@ OnionChopper A Modular Arithmetic Hardware Accelerator for Private Information Retrieval

Goal

Setup: A client wishes to retrieve a record from a server's database without revealing to the server which record was accessed.

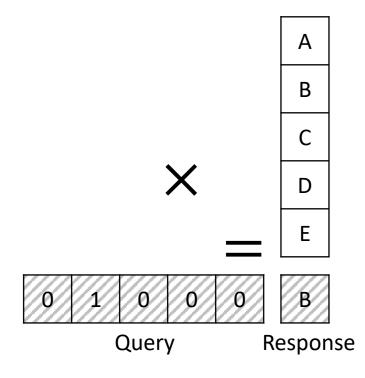
Applications: Private voice calling, media streaming, advertising impressions, online presence indicators, etc

Problem: Slow runtime due to privacy preservation and cryptographic operations

Solution: OnionChopper: a small, fast, and energy efficient hardware accelerator

Private Information Retrieval

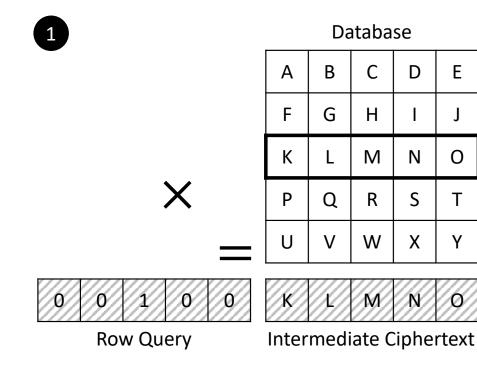
A typical Private Information Retrieval (PIR) scheme has the client send a one-hot vector encryption of the location of the desired record in the database. Database

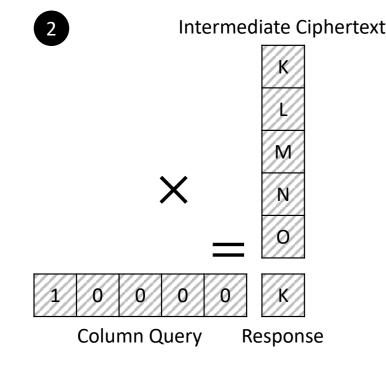


Basic PIR Scheme

After multiplication with the database, the response is an encryption of the desired record.

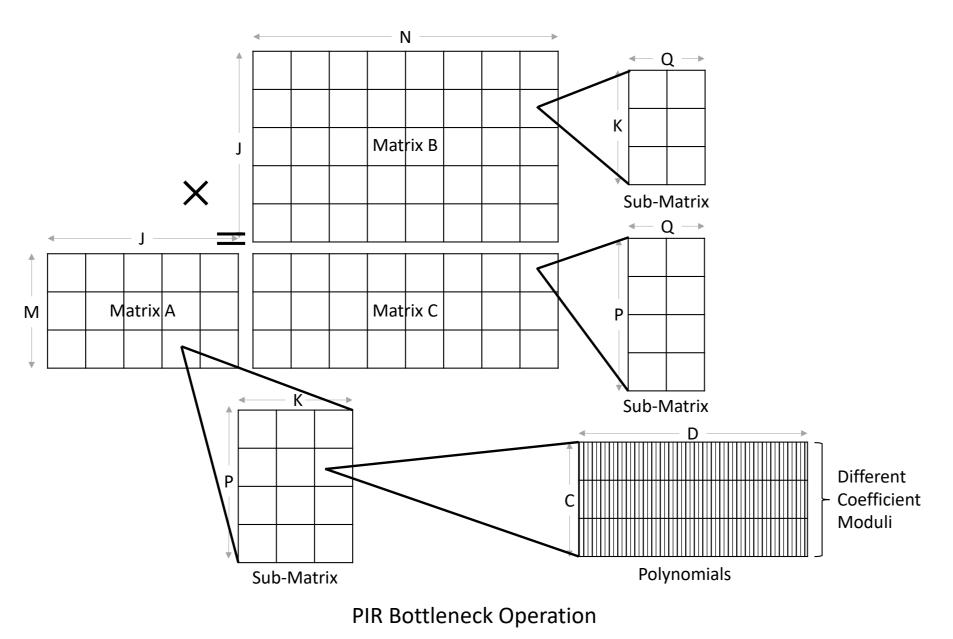
The database can be reconceptualized as multi-dimensional, and multiple query vectors sent to indicate the row and column.





OnionPIR

OnionPIR uses two different types of ciphertexts in its multiplications to keep response size small. OnionPIR's ciphertexts can be represented as matrices of collections of polynomials.

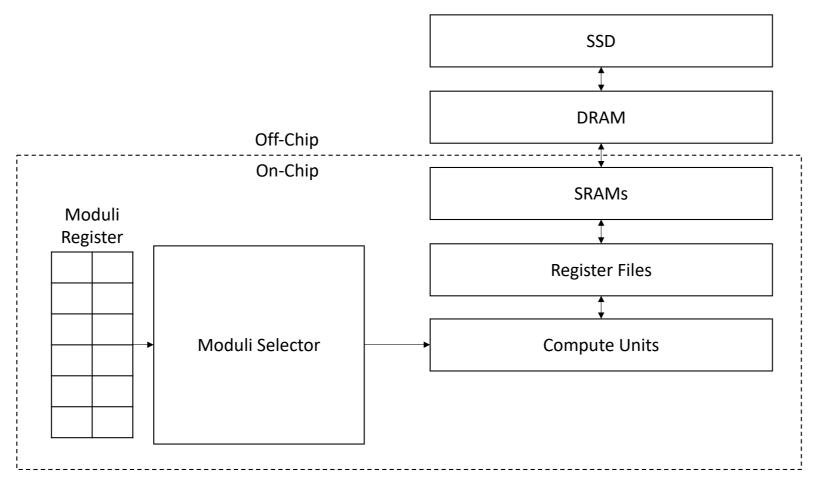


The same bottleneck operation – a matrix multiplication of submatrices of polynomial collections – is applicable to other PIR schemes.

Hardware Design

Our proposed hardware design is a near-storage accelerator operating at 2GHz located in an SSD unit that contains a DRAM.

Beyond the DRAM will be a storage hierarchy of SRAMs and register files that aims to take advantage of data reuse.

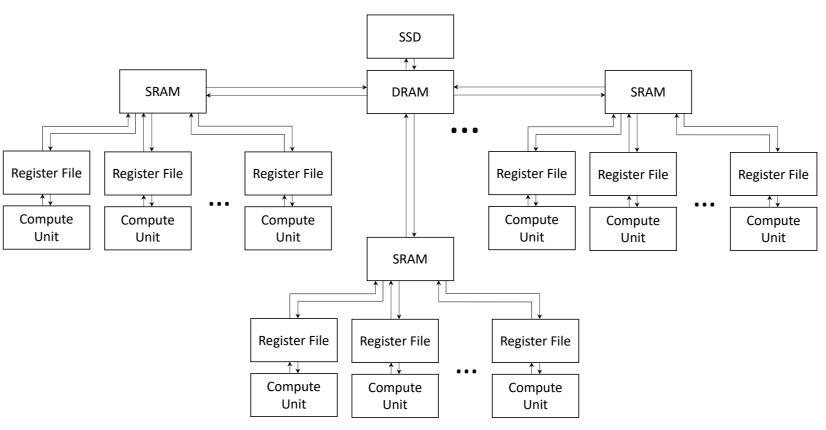


2D PIR Scheme

Georgia Shay, Massachusetts Institute of Technology

Optimization

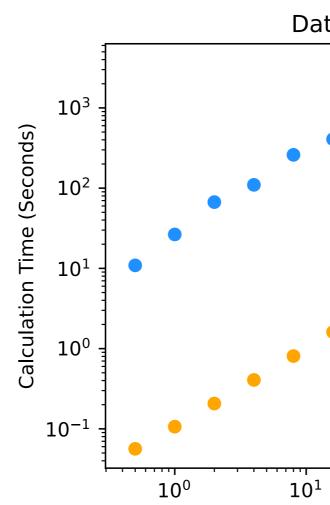
The Timeloop tool can evaluate the best loop ordering and tiling for a tensor calculation on a given architecture, and determine how much area, energy, and time it takes. We defined a range of reasonable values for the storage level sizes and fanouts, and ran the tool on these possible architectures.



The fastest architecture with the best area-energy product had two 4KB SRAMs and two 32-register register files. It takes up 6.87mm² in area, largely from the compute units, which are heavily pipelined.

Results

On a 64GB database, OnionPIR's main operation takes 6.4 seconds on OnionChopper, compared to 29 minutes on a general-purpose processor.



OnionChopper Hardware Design

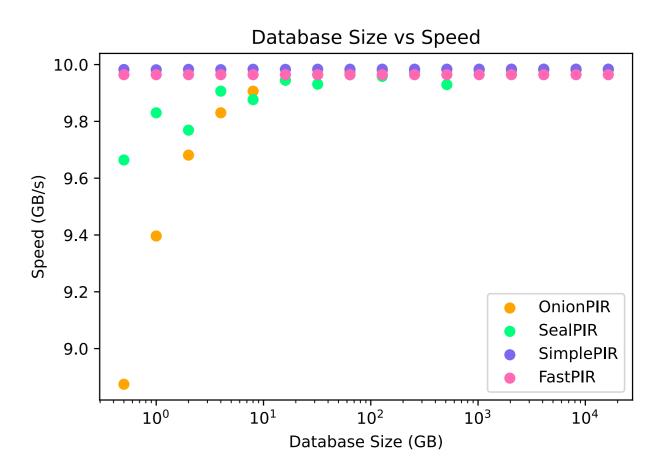


Storage Hierarchy

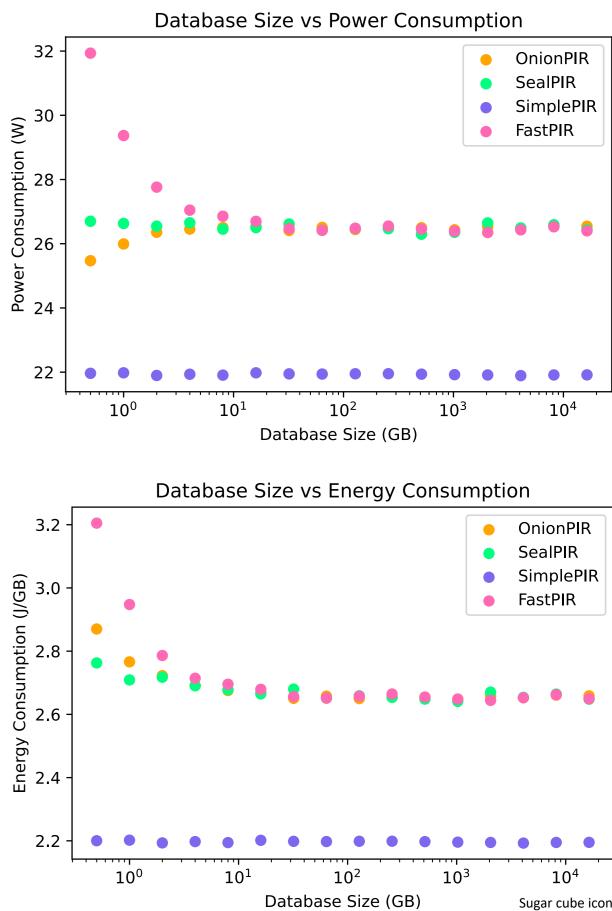
Database Size vs Speed General Purpose Processor OnionChopper Accelerator 10^{4} 10^{2} 10^{3}

Database Size (GB)

OnionChopper is very extensible to different database sizes, approaching the SSD bandwidth of 10GB/s at large database sizes on OnionPIR and other algorithms.



The on-chip accelerator consumes 169J, or 26W during the calculation, roughly one-third of which is from the compute units and two-thirds from the SSD. Similar consumption is found at large database sizes and for other protocols.



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		SimplePIR			
		FastPIR			
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